

About Credo

Credo (CRDO) has a mission is to deliver high-speed solutions to break bandwidth barriers on every wired connection in the data infrastructure market. We provide innovative, secure, high-speed connectivity solutions that deliver improved power and cost efficiency as data rates and corresponding bandwidth requirements increase exponentially throughout the data infrastructure market.

or you can find it in Company - Credo (credosemi.com)

Company - Credo

According to International Data Corporation (IDC), the amount of data created, captured, copied, and consumed in the world is expected to increase by approximately 2.8 times, from 64 ZettaBytes (ZB), in 2020 to more than 179ZB in 2025.

credosemi.com

Job Title: Physical Design Engineer

Duties

- Perform RTL to GDSII design flow, including floor planning, power grid design, place and route, clock tree synthesis, timing closure, power/signal integrity signoff, EM/IR.
- Perform Full chip DRC/LVS
- Automate the design flow to promote efficiency, improve RTL to GDS design flow;
- Participate in next generation physical design, methodology and flow development.

Requirements

- BSEE/MSEE with minimum 1-year of P&R experience by using SoC Encounter.
- Successful track records of taping out 40/28/16 nm chips;
- Familiar with DC, PT, DFT is prefer;
- Be familiar with RTL to GDSII design flow;
- Be familiar with EDA tool, such as ICC or Soc encounter;
- Be familiar with computer languages such as Perl/TCL/C-shell;
- Self-motivated with good communication skills and team spirit.

Application Method

We are looking for people with good working attitude, team spirit and strong communication skills. If you are interested in Credo and meet any requirements above, it's our pleasure to have you join our work.

Email	jobs@credosemi.com
Post	Unit 221, 2/F, Core Building 2, Phase one, Hong Kong Science Park

Only short-listed candidates will be notified.

Personal data provided by applicants will be used for recruitment purposes only.