Openings – Analog IC Design Engineer

Job Description and Responsibilities:
• Build analog component model, transistor-level circuit design, simulation and verification
• Complete critical layout design and supervise layout team member, perform post layout verification
• Prepare design review documents and participate in review
• Define test plan for bench test and automated test.

Job Requirements:
• Bachelor degree or above in microelectronics, physics, electronic engineering and other related majors.
• Experience in layout design, and successful tape out and testing is a highly recommended.
• Have the following analysis and design experience of one or multiple analog circuit modules: operational amplifier, switched capacitor circuit, reference voltage circuit, on-chip power supply, PLL clock circuit, ADC, DAC, ESD circuit, I/O PAD circuit;
• Familiar with using cadence simulation and layout tool.
• Familiar with the structure, technique and principles of CMOS analog circuits; design and verification process of functional blocks;
• Capable of self-learning, have innovative spirits, possess good communication skills and team spirit. Able to co-operate and work closely with remote team members
• Be honest, strong sense of responsibility and integrity

Recommended Course Requirement:
• UG: ELEC 4420 Analogue Integrated Circuits Design and Analysis
• PG: ELEC 5040 Advanced Analog IC Analysis and Design
Openings – Digital IC Design/Verification Engineer

Job Description and Responsibilities:
• Perform Logic Design, develop RTL codes and complete design verification for modules based on system specifications
• Complete logic synthesis and timing analysis. Perform post-synthesis verification
• Support back-end engineers for place and route of modules
• Support analog design engineer for mixed-signal co-simulation
• Prepare design review documents and participate in review
• Define test plan for bench test and automated test

Job Requirements:
• Bachelor degree or above in microelectronics, physics, electronic engineering and other related majors
• Have the design experience with ARM related modules, such as I2C, UART, GPIO, Single-wired port, memory interface etc, is considered to have advantage
• Familiar with Synopsys tools and Cadence tools;
• Capable of self-learning, have innovative spirits, possess good communication skills and team spirit. Able to co-operate and work closely with remote team members
• Be honest, strong sense of responsibility and integrity;

Recommended Course Requirement:
• UG: ELEC 4410 CMOS VLSI Design
• PG: ELEC 5160 Digital VLSI System Design and Design Automation
How to apply

• Application email to: adminhk@atom-semiconductor.com

• With the following information:
  • Desired position:
    • Analog IC Design Engineer
    • Digital IC Design Engineer
    • Digital Verification Engineer
  • Resume
  • Cover letter
  • Latest unofficial transcript, and publication list