

Tetramem Introduction

TetraMem is a fast growing venture-backed well-funded startup company working on the next generation computing platforms. We are hiring in multiple positions.

In this team you be part of a world-leading software and IC design team responsible for the development and deployment of software and hardware solutions for a revolutionary computing system. We offer a very competitive compensation and benefits package (including medical, unlimited PTO, and 401k) that commensurate with experience.

Compiler Software Engineer

Requirements:

- MS with 5+ years of experience or PhD in Computer Engineering with emphasis on computer science
- Experience with C, C++, or python development
- Experience with popular compilers such as GCC, Clang or ML compilers such as Glow, TVM, or TensorFlow
- Experience with LLVM
- Ability to work in startup environment and work both independently and as a team player, with the ability to provide technical leadership to other members of the engineering team.

Experience in one or more of the following areas considered a strong plus:

- Publication in top relevant conferences on software development.
- Hands-on experiences with open source compiler libraries and tools.
- Hands-on experience with SDK development for CPU/GPU/ASIC.

ASIC RTL / SoC Design Engineer

Requirements:

- MS with 5+ years of experience or PhD in Electrical Engineering with emphasis on RTL/SoC/digital design
- Experience with Verilog and system Verilog
- Experience with VCS,
- Experience with pre-layout simulation and post-layout simulation
- Understanding of the design flow. Ability to work with the backend team
- Familiarity with AMBA APB AXI Protocol
- Familiarity with RISC/Arm or other core architectures
- Ability to create innovative architecture and solutions to customer requirements
- Ability to work in startup environment and work both independently and as a team player, with the ability to provide technical leadership to other members of the engineering team.

Experience in one or more of the following areas considered a strong plus:

- FPGA/ASIC design of image processing systems
- Working knowledge of SoC architecture such as CPU, GPU or accelerators
- Familiarity with: UVM, place-and-route, STA, EM/IR/Power

Analog/Mixed-Signal IC Designer

Requirements:

- MS or PhD in Electrical Engineering with emphasis on CMOS analog/mixed-signal integrated circuit design
- 5+ years of experience in analog or mixed-signal IC development in advanced CMOS processes (PhD experience may be considered as experience), successful tape-out experience with leading foundries
- Strong knowledge in analog/mixed signal circuits such as Op-Amp, bandgap, oscillator, clock distribution
- Familiar with common EDA environment tools, CAD tools and Analog design methodology including design, simulation, layout, and verification tools (e.g. Synopsys, Cadence, Mentor Graphics etc.).
- Ability to create innovative architecture and circuit solutions to customer requirements
- Ability to work in startup environment and work both independently and as a team player

Experience in one or more of the following areas considered a strong plus:

- Hands-on experience and in-depth knowledge in CMOS low-power and high-resolution data converter (ADC/DAC) design and techniques.
- Experience with memories, especially emerging non-volatile memory