

Power Semiconductors Development Engineer/Trainee

Steifpower Technology is a professional fabless design company that designs and produce power semiconductor devices. The team has very rich experience in power field and comes from the world leading semiconductor company in German, Italy, Taiwan and US.

Steifpower Technology owner a wide range of technologies including cutting-edges technologies as GaN and SiC MosFet and Diodes.

With a strong knowhow in power devices technologies, Steifpower Technology's technologies portfolio now spans the main power technologies present in the power semiconductors market, this includes SG power MosFet, IGBT trench FS, GaN MosFet and SiC Schottky diodes. From headquarters in Hong Kong and offices around the world, Steifpower Technology's team explores new ways to make power electronics devices for the power electronics world smaller, thinner, faster and better.

Job Summary

As a member of Steifpower Technology's Technology Development Organization this individual will drive the development and optimization of submicron Shield Gate power trench MosFet and trench IGBT technologies. The engineer/trainee will work closely with Steifpower Technology's R&D team, Product Development group and our foundry partners to deliver best in class semiconductor technology products.

Roles & Responsibilities

This position has responsibility for:

- Learn and develop on new technologies
- Drives the technology qualification, working with Quality Assurance and Product Development teams
- Perform DC/AC characterization and standardize analysis techniques for SG power MosFet, IGBT trench FS technologies
- Develop test-chips for technology validation using external foundries
- Define and drive electrical specifications of new technologies; maintain electrical specifications for released technologies
- Work closely with foundry engineers to validate silicon electrical performance of external foundries
- Perform TCAD simulation to support the research and development of next generation technology

Minimum Qualifications (Experience and Skills)

- Fresh/ minimum of 2+ years' experience in semiconductor industry
- In depth understanding of power semiconductor devices physics, in particular Mos technologies/devices
- Background knowledge with Mos and bipolar, and some foundry processes
- Good data analysis, problem solving, and communication skills
- Familiar with Layout tools
- Familiar with DC/AC device characterization equipment
- Experience with TCAD 2D/3D process and device simulation tools
- Experience with the Microsoft Office programs

Preferred Qualifications

- Good teamwork, internally as well as with foundries

Working locations

- Hong Kong
- Occasionally travel to US, TWN, China if necessary

Education Requirements

- Bachelor/ Master/ PHD's degree in Electronics Engineer or Physics; Applied Physics;
- **Fresh graduates are also welcomed, relative training program will be supported**
- **Foreign students are also welcome**

Work Environment

This job operates in a professional office environment. This role routinely uses standard office equipment.

Contact

Send your personal CV to David TANG/ Domenico Lo Verde @ "career@steifpower.com"