



Position: Senior / Digital IC Design Engineer (Place & Route)

Location: Hong Kong / Shenzhen / Taiwan

Job Responsibilities:

- Perform logic synthesis, DFT, static timing analysis, place & route, library characterization
- Support ATPG and functional test program development, test coverage analysis
- Support other physical design and verification tasks
- Support wafer level testing and silicon evaluation

Job Requirements:

- Bachelor degree or above in Electronic Engineering or equivalent
- 5 years or above solid experience in digital implementation flow
- Proven track record of implementing design through synthesis, floor-planning, place & route, extraction, timing, and physical verification
- Strong understanding of constraints generation, STA, timing optimization, and timing/SI closure
- Experience with multi-power domain designs
- Knowledge of IR/EM closure
- Strong scripting skills toward flow customization and efficiency
- Candidate with less experience will be considered as Digital Design Engineer

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: recruit@solomon-systech.com, or
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.