

IC Design Engineer Job Details



• Job Responsibilities:

- Participate in New Product Spec Definition;
- Construct System-Architecture; Select suitable FAB Technology;
- Compose New Product Development Plan with sound schedule;
- Apply top-down design methodology and design IP modules to complete the whole schematic database with respective simulation verifications;
- Compose test-plan, conduct review with TE and AE to confirm silicon validation plan;
- Instruct layout engineers to complete the error-free layout with respective checking procedure;
- Carry out post-layout simulation and facilitate tapeout;
- Join evaluation with TE and AE, facilitate for production and product introduction to the market;
- Facilitate on datasheet composition;
- Facilitate AE and FAE to resolve, if any, customer' s product application issue;

• Job/Skill Requirements:

- Micro-electronics, Integrated-Circuit related curriculum, MSc/MPhil/PhD Degree holders;
- Familiar with Analog IC design basic theories;
- Familiar with Semiconductor Device Physics, modern FAB technologies;
- Familiar with using Analog IC Design and Layout EDA tools;
- Good command in written Chinese and English, speech of Putonghua and English;
- Good team spirit;

• CV Submission:

- ricky_mak@sg-micro.com in English;
- **Until 12 Dec, 2022;**

• Interview Schedule:

- **No later than 9 Feb, 2023;**