



### **Huawei Hong Kong Research Center**

Huawei Hong Kong Research Center was established in 2018, with focus on CPU, Basic Software, AI related research and development, from algorithm, software to chip set development. We are looking for high caliber Engineers and Researchers at all levels to join our new team.

### **Senior Engineer / Engineer/ - Analog Layout Design**

#### **【Job Responsibilities】**

- Responsible for custom analog layout design
- Responsible for layout verification (DRC/ERC/LVS/EMIR/EOS, etc.) and post-layout simulation extraction
- Responsible for the tape out of the final GDS and the e\_job view of the mask

#### **【Job Requirements】**

##### **Skills requirements:**

- Bachelor degree or above in Electronics related discipline, with over one year of work experience
- Familiar with mainstream IC layout design and verification tools
- Good communication skills, learning skills, analytical skills and teamwork skills

##### **Knowledge requirements:**

- Familiar with integrated circuit CMOS process flow and basic knowledge of circuits

- Proficient in layout design knowledge, and clear understanding of parasitic, noise, devices, high-precision matching knowledge
- Experience in ESD, Latchup, and familiar with SKILL/Shell/Python scripting language are preferred
- Successful IC tape-out and mass production experience are preferred

For interested parties, please send your full resume and salary expectation to [lin.haoyao@huawei.com](mailto:lin.haoyao@huawei.com). Applicants who are not invited for interview within 6 weeks from the closing date may assume their application unsuccessful.