模拟芯片设计工程师

【工作职责】

- 理解客户需求，承接团队的芯片方案任务
- 负责模拟芯片关键 IP（如 AFE、PMU、ADC 等）的电路设计、验证、优化等工作，确保电路设计满足规格要求，以芯片最后交付为目标

【任职要求】

业务技能要求:

- 电子专业本科、硕士或博士；一年以上工作经验
- 扎实的模拟电路理论基础，熟悉各类基本单元电路设计
- 有 ADC/PMU 相关设计经验优先
- 有成功投片经验与芯片量产经验优先

专业知识要求:

- 扎实的电路设计基础，具有射频芯片电路相关知识，EM 仿真能力，熟悉模拟/混合信号开发流程，掌握相关设计工具
- 熟练掌握模拟电路（AFE、PMU、ADC 等）专业知识
Analog Chip Design Engineer/Intern

[Job Responsibilities]

- Understand customer’s needs to deliver IC solution
- Responsible for key analog IP (e.g. AFE, PMU and ADC etc.) circuit design, verification and optimization etc., and targeting to meet specifications and the final delivery of the IC

[Job Requirements]

Skills requirements:

- Bachelor, Master, or Doctorate in electronics; over a year of work experience in relevant fields
- Solid theoretical foundation of analog circuits, and familiar with circuit design of various basic building blocks
- ADC/PMU related design experience is preferred
- Successful IC tape-out and mass production experience are preferred

Knowledge requirements:

- Solid foundation on circuit design with RFIC knowledge and EM simulation capability, familiar with analog mixed-signal IC development process, and mastering relevant CAD tools
- Proficient professional knowledge in analog circuits (e.g. AFE, PMU and ADC etc.)

For interested parties, please send your full resume and salary expectation to lin.haoyao@huawei.com. Applicants who are not invited for interview within 6 weeks from the closing date may assume their application unsuccessful.