



Huawei Hong Kong Research Center

Huawei Hong Kong Research Center was established in 2018, with focus on CPU, Basic Software, AI related research and development, from algorithm, software to chip set development. We are looking for high caliber Engineers and Researchers at all levels to join our new team.

模拟芯片版图设计工程师

【工作职责】

- 理解客户需求，承接团队的芯片方案任务
- 负责产品的芯片版图设计，验证，优化等工作，确保版图设计满足电路规格与晶圆工艺要求，以芯片最后交付为目标

【任职要求】

业务技能要求：

- 微电子专业本科或硕士；一年以上工作经验
- 理解各类半导体器件的工作原理及基本电路
- 有芯片产品版图设计经验者优先
- 有芯片流片成功及量产经验者优先

专业知识要求：

- 全定制芯片版图设计，负责从底层模块到顶层芯片集成的版图设计和验证
- 熟练芯片版图设计与验证工具

Analog IC Layout Design Engineer/ Intern

【Job Responsibilities】

- Understand customer' s needs to deliver IC solution
- Responsible for product level IC layout design, verification and optimization etc., assure the layout design can meet the circuit specification and wafer process requirement, and targeting the final delivery of the IC

【Job Requirements】

Skills requirements:

- Bachelor or Master in microelectronics; over a year of work experience in relevant fields
- Understand the working principle of various semiconductor devices and basic electric circuit
- Possess product level IC layout design experience is preferred
- Successful IC tape-out and mass production experience are preferred

Knowledge requirements:

- Fully customized IC layout design, responsible for the layout design and verification from the bottom level modules to the top level chip integration
- Proficient in IC layout design and verification tools

For interested parties, please send your full resume and salary expectation to chan.ho.wai@huawei.com and chan.ting.ting@huawei.com. Applicants who are not invited for interview within 6 weeks from the closing date may assume their application unsuccessful.