



About Credo

Credo (CRDO) has a mission is to deliver high-speed solutions to break bandwidth barriers on every wired connection in the data infrastructure market. We provide innovative, secure, high-speed connectivity solutions that deliver improved power and cost efficiency as data rates and corresponding bandwidth requirements increase exponentially throughout the data infrastructure market.

or you can find it in [Company - Credo \(credosemi.com\)](http://Company - Credo (credosemi.com))

Company - Credo

According to International Data Corporation (IDC), the amount of data created, captured, copied, and consumed in the world is expected to increase by approximately 2.8 times, from 64 ZettaBytes (ZB), in 2020 to more than 179ZB in 2025.

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Job Title: DFT Engineer

Duties

- Implement basic DFT schemes, including scan insertion, boundary scan, Mem BIST, DRC clean, ATPG and pattern simulation,
- Support ATE bring-up, and debug the ATE patterns for production flow,
- Support logic BIST, Memory BIST diagnosis for yield improvement.

Requirements

- Bachelor degree or above in Electronic Engineering.
- Good understanding of synthesis and timing.
- Good understanding of IPs, integration and verification.
- Team spirit and strong communication skills.
- Enjoy challenging work and a Self-motivated good team player.
- Good programming in Perl, TCL and Shell programming is preferred.
- Hand-on experience in Synopsys (DFT Compiler/TetraMax) or Mentor Tessent is preferred.
- Good understanding of microprocessors, and computer system architecture is preferred.

Application Method

We are looking for people with good working attitude, team spirit and strong communication skills. If you are interested in Credo and meet any requirements above, it's our pleasure to have you join our work.

Email	<u>jobs@credosemi.com</u>
Post	Unit 221, 2/F, Core Building 2, Phase one, Hong Kong Science Park

Only short-listed candidates will be notified.
Personal data provided by applicants will be used for recruitment purposes only.