



ADVANCED POWER SEMICONDUCTOR TECHNOLOGY (HK) LIMITED

先進功率技術(香港)有限公司

Introduction

Advanced Power Technology (Hong Kong) Co., Ltd. is formed by a senior team with more than 20 years of experience in the analog and power semiconductor industries. The founder Dr. Shuai Zhang has served as the head of R&D of TSMC (China) Co., Ltd., Hua Hong NEC, who is now the dean of Chengdu Minshan Power Semiconductor Industry Technology Research Institute and a practical professor the Hong Kong University of Science and Technology. The company has R&D and sales centers in Hong Kong, Shanghai, Hangzhou, Chengdu, Shenzhen.

The company focuses on the development of high-performance analog and power semiconductor technology, products and system solutions. Products include power switches, smart power switches, DC/DC, AC/DC, Gate Driver and high-performance high-frequency power modules. The main target application areas are consumer electronics, various power supplies, automobiles, home appliances and green energy industry etc. The company has strong process technology and device development capabilities, as well as world-class fab resources and packaging and testing plant resources. We strive to build an analog and power semiconductor company that combines process technology, device design, circuit design and system applications, and provide customers with a complete series of analog and power semiconductor products and application solutions, breaking the monopoly of international manufacturers, and realizing localization in this field.

Now we sincerely invite:

1. Head/Senior/Junior Analog Power IC Design Engineer

Responsibility:

- work with marketing and FAE to define the analog and power IC product line,
- Build and lead IC designer to complete the product development on schedule,
- follow and construct the product development procedure,
- On-site support the FAE and customer when need.

Requirement:

- master degree above, graduated from EE, electronics and circuit. 15 years work experience in analog and power IC product design,
- Skilled in circuit simulation tools,
- Skilled in layout tools, such as laker,
- Have more than 15 years power IC design experience, such as DC/DC, AC/DC, LDO, Gate driver, can define the product topology,
- Know well in IC test and demo board verification procedure,
- Basic knowledge in semiconductor device and process flow,
- Junior can apply as analog power IC Design Engineer.



2. Senior Device Development Engineer

Responsibility:

- Be in charge of device design and development, including trench MOSFET, SGT, Super Junction MOSFET, LDMOS, IGBT, and other devices,
- Work including process simulation, device simulation, layout, device characterization, device reliability and qualification,
- Release the device design rule to other team.

Requirement:

- master degree above (PHD preferred), graduated from EE, physics, electrical material science and related field. 6 years work experience in power device design and simulation,
- Skilled in TCAD software, such as Silvaco Taurus or Sentaurus,
- Can draw test pattern layout with layout tools, such as laker, virtuoso,
- Skilled in device characterization instruments, such as B1505, HP4155, HP8753D and HP4284,
- Basic knowledge in semiconductor process flow.

Attractive staff benefits, profetional training, bonus, stock options, etc., will beprovided

Interested parties please contact:

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