ViXS Systems Hong Kong Limited
Job position: ASIC Design Engineer

Responsibilities:

- As a member of the engineering design team, the successful candidate will focus on the development of our next generation IC designs
- The successful candidate will take ownership of various VLSI design responsibilities including: research, logic design, verification, synthesis and documentation.

Qualifications:

- Masters/Bachelor Degree in Electrical/Computer Engineering or equivalent
- Proficient oral and written communication skills in English
- Candidates who possess the following experience/knowledge is a plus:
  - Hands on experience with ASIC design tools (e.g. dc_shell, physical complier, VCS)
  - literacy in Verilog or VHDL
  - Researched on any of the following topics:
    - 2D/3D conversion
    - Frame Rate conversion
    - Image Processing
    - De-interlace
    - De-noise

Application method:

Please send full resume via email to eleung@vixs.com. Applicants who graduated less than 3 years ago must include transcripts in the application. Only shortlisted candidates will be contacted. All information collected will be treated in strict confidence and used only for recruitment purposes.

For further information, please visit http://www.vixs.com

WWW.VIXS.COM

ViXS Systems HK Limited
Job position: ASIC - Senior Physical Design Engineer

Requirements:
- BSEE or equivalent in electronic or computer engineering
- Experience with physical design methodology and tools in these areas: logic synthesis, place and route, RC extraction, power analysis, physical verification and static timing analysis
- Excellent analytical and organizational skills
- Good communication skill in spoken and written English
- Be a team player working in a dynamic environment

Responsibilities:
- Responsible for various aspects of physical implementation of advanced SoC ASICs targeted at multimedia processing and networking markets.
- Participate in establishing physical design methodologies, flow automation, chip floorplan, place and route, timing closure, power analysis and physical verification.

Application method
Interested candidates please email resume, cover letter and transcripts to eleung@vixs.com, with title
"Application: Senior Physical Design Engineer"

ViXS Systems Hong Kong Limited
Job position: Embedded Software Engineer

Responsibilities:

- Research and Develop the firmware, the kernel level driver and the embedded application software for the XCode family, a video/audio hardware accelerators.
- Implement algorithm to control hardware blocks for the video, audio and system layer standards such as ISO-13818, ISO14496 and ITU-T H.264, and turn the specifications into real applications
- Develop system on chip (SOC) solutions for multimedia applications
- Adapt and create new Linux kernel drivers for SOC solutions
- Implement new features in Linux kernel drivers

Qualifications:

- Bachelor or Master degree in computer science, computer or electrical engineering, or related discipline
- Proficient programming experience in C language
- In-depth experience in programming on Linux and Windows platform; embedded programming experience is preferred.
- Sound knowledge in video/audio encoding and decoding
- Strong analytical and problem-solving skills
- Proficient oral and written communication skills in English
- Experience in Linux kernel and kernel driver programming and debugging is a plus
- Knowledge of MPEG2/MPEG4/AVC standard is a advantageous
- Fresh grad with intership or work experiences is a advantageous

Candidates with more experience will be considered for the Senior Engineer position.

Application method:

Please send full resume via email to clau@vixs.com. Only shortlisted candidates will be contacted. All information collected will be treated in strict confidence and used only for recruitment purposes.

For further information, please visit http://www.vixs.com

ViXS Systems Hong Kong Limited
Job position: Video/Audio Algorithm Engineer and Architect

If your interest is cutting-edge video/audio algorithms and implementation and you want to see your invention implemented in silicon, you will find ViXS a fantastic environment.

Working in a team of audio/video algorithms and implementation experts, you will build next generation innovative video ASICs from the algorithm up.

The successful candidate will have a strong algorithm and C/C++ implementation background and meet the following requirements:

Requirements:

- B.Sc, M.Sc, or PhD in Electrical/Computer Engineering or equivalent
- Expertise in video compression standards, e.g. HEVC / H.264 / On2VP8 / VC-1 / AVS / MPEG4 part2 / MPEG2
- Expertise in audio compression standards, e.g. HE-AAC / AAC-LC / EAC3 / AC3 / MP123 / DTS / Dolby TrueHD / DTS-HD MA
- C / C++ / MATLAB programming skills is a must
- DSP implementation and optimization experience preferred
- Embedded system development preferred
- ASIC/hardware experience preferred
- Proficient oral and written communication skills in English

Candidates with more experience will be considered for senior position.

Application method:

Please send full resume via email to wzhao@vixs.com. Only shortlisted candidates will be contacted. All information collected will be treated in strict confidence and used only for recruitment purposes.

For further information, please visit http://www.vixs.com

WWW.VIXS.COM

ViXS Systems HK Ltd.
Job position: RFIC Design Engineer

Requirements:
- Ph.D / Master Degree in Electronic or equivalent,
- Strong knowledge of engineering fundamentals,
- Strong knowledge of RF analog circuit design (LNAs, Mixers, PAs, PLLs)
- Strong knowledge of MOS transistors characteristics, noise and analog circuit design,
- Proficiency in layout and layout verification (DRC, LVS),
- Laboratory skills (PCB design review, soldering, general lab debug),
- Laboratory measurement skills (analog, RF, as required)

Responsibilities:
- Development of RF technology from concept to full production silicon in the areas of WLAN, MoCA, DOCSIS and ISDBT,
- Design of radios and radio building blocks in various digital deep-submicron CMOS technologies,
  (RFIC building blocks design such as LNAs, PAs, RF Synthesizers and PLLs, IF and baseband filters, AGC, etc.)
- Design of RF board and silicon characterization.
- Participation in design reviews and striving to improve design flow and methodology.

Application method
Interested candidates please email resume, cover letter and transcripts to mcave@vixs.com and kmak@vixs.com, with title
"Application: RFIC Design Engineer"

ViXS Systems Inc. is a multimedia solutions innovator providing technologies for processing, managing, securing and distributing high quality video and audio allowing seamless multimedia control, conversion, and connectivity between any class and size of digital entertainment device.

ViXS Systems supplies advanced System-on-a-Chip semiconductors, software solutions and hardware reference designs for the world's top manufacturers of Digital TVs, DVDs, Set-top boxes, Personal Video Recorders, PCs, Network Attached Storage devices, Residential/Home Gateways and Blu-Ray players/recorders.

ViXS Systems is a leading fabless semiconductor company headquartered in Toronto, Canada with global operations and offices in Europe, Asia and North America. ViXS has filed more than 334 patents worldwide with over 103 patents being issued to date. Listed as one of the Deloitte's fastest growing North American companies, ViXS is setting new standards in the way digital entertainment is viewed and transmitted across an endless array of multimedia products.